

test conditions -	No signal. All controls at minimum 8ohm (wirewound) resistive load on output
date -	09/09/15

			pin	V-plate	pin	V-cath	pin	V-grid	V-Rsense	R-sense	I-plate mA (1)	
Tung-sol reissue	5751	1st pre	V1a	1	102	3	1.23	2	0	n/a	2200	0.559
		2nd pre	V1b	6	89.8	8	1.13	7	0	n/a	1800	0.628
Tung-sol reissue	5751	1st OD	V2a	1	89.8	3	1.05	2	0	n/a	2700	0.389
		FAT	V2b	6	119	8	1.55	7	0	n/a	1800	0.861
RCA (NOS)	5879	OD	V3	8	104	3,9	0.56	1	0.07	n/a	470	1.191
GE (?OS)	12AY7	FX return	V4a	6	105	8	1.77	7	0	n/a	1500	1.180
		FX send	V4b	1	223	3,9	47.7	2	0	n/a	28800	1.656
Tung-sol reissue	12AX7	PI pos	V5a	6	178	8	24	7	0	n/a	10517	2.282
		PI neg	V5b	1	187	3		2	0	n/a		
Tung-sol reissue	5881	PT pos	V6	3	374	8	34.4	5	0	0.0428	0.93	46.022
Tung-sol reissue	5881	PT neg	V7	3	374	8	34.4	5	0	0.0425	1.05	40.476

			ref/des.	Vdc	Vac (2)
TAD	GZ34	rect Vin	Z1	n/a	
n/a	P/S	@R1-C2	"A"	377	
n/a	P/S	@L1-C31	"B"	376	
n/a	P/S	@R2-C32	"C"	288	
n/a	P/S	@R3-C33	"D"	223	
n/a	P/S	@R4-C34	"E"	158	
n/a	P/S	@R5-C35	"F"	157	

1. "Iplate" is formula. Based on, $V-Rsense$ (or $V-cath$) / $R-sense$ (or $R-cathode$) * 1000 = $I-plate$
2. "Vac" is either actual AC @ rectifier input or AC ripple on P/s stages